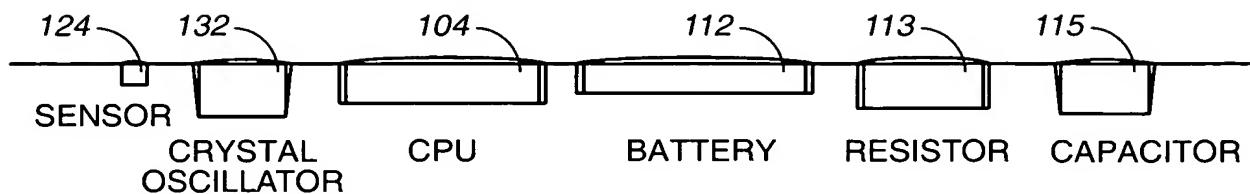


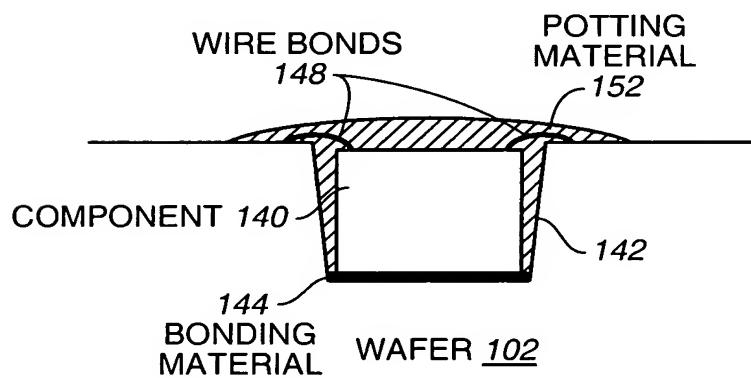
FIG. 1A

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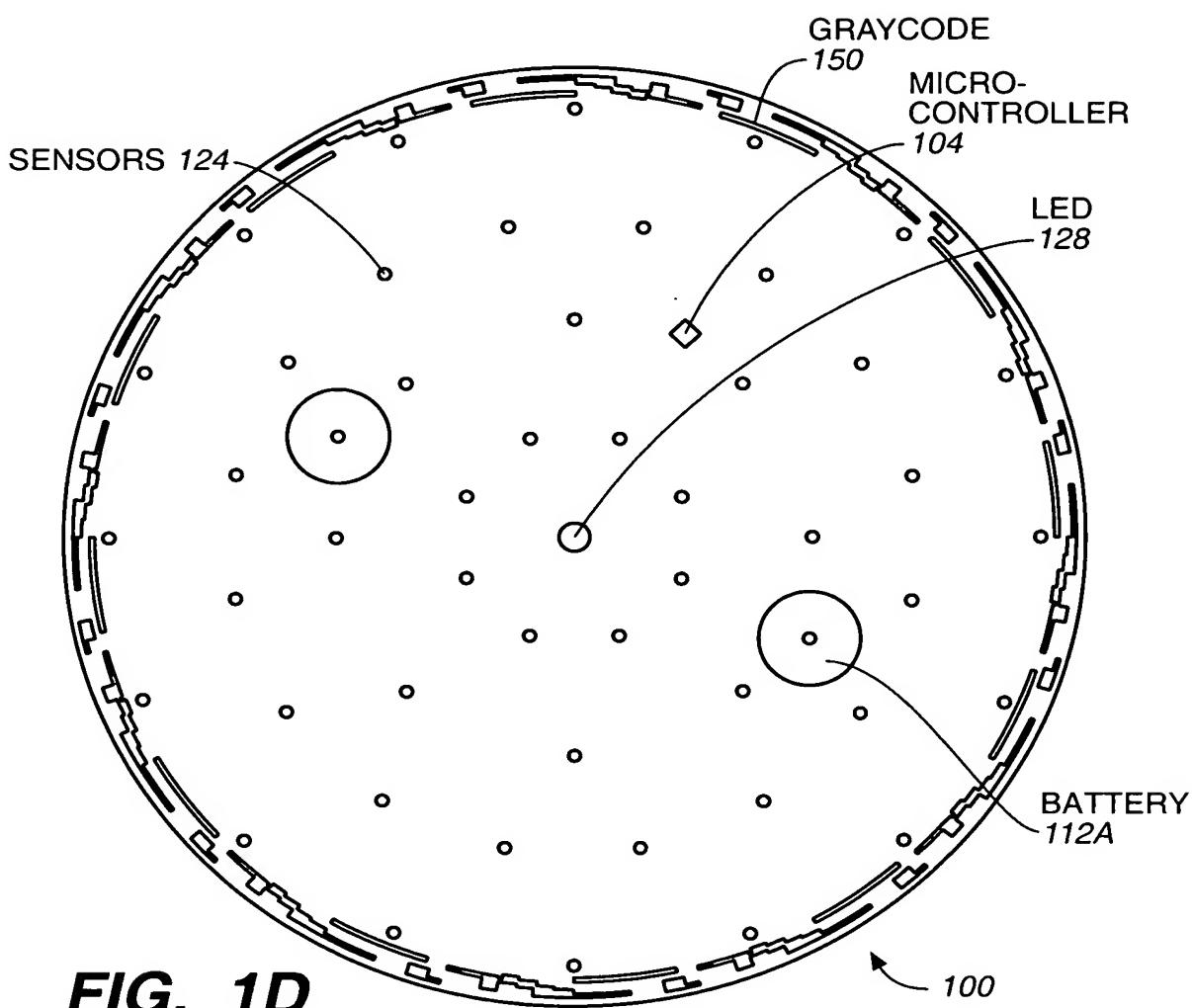
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**FIG.\_ 1B**

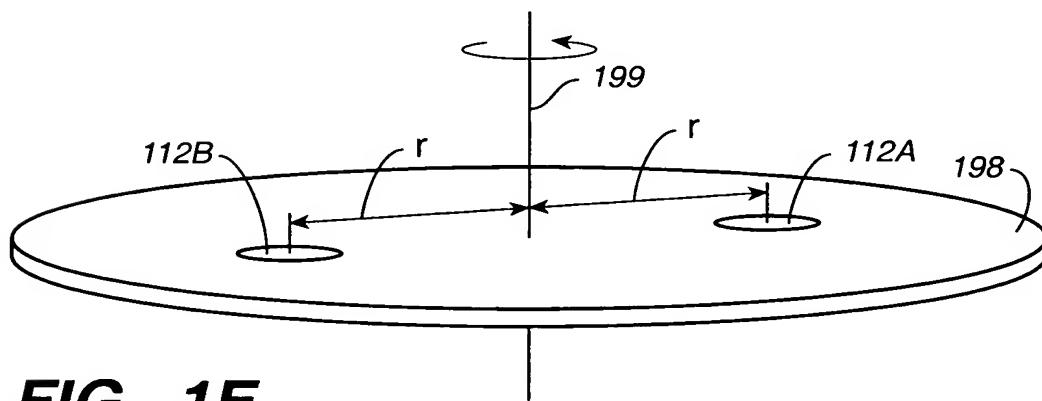


**FIG.\_ 1C**

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**FIG. 1D**



**FIG. 1E**

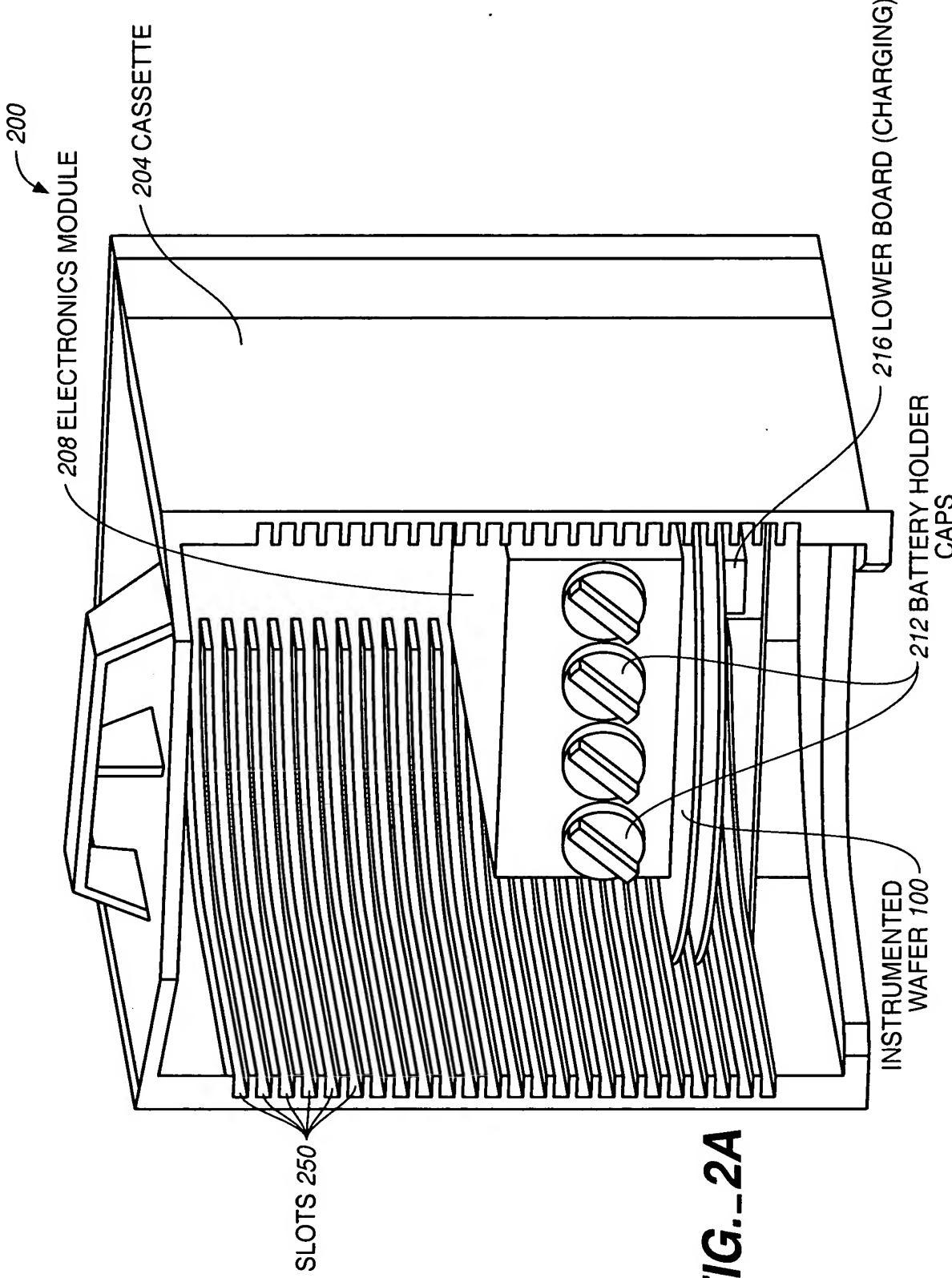
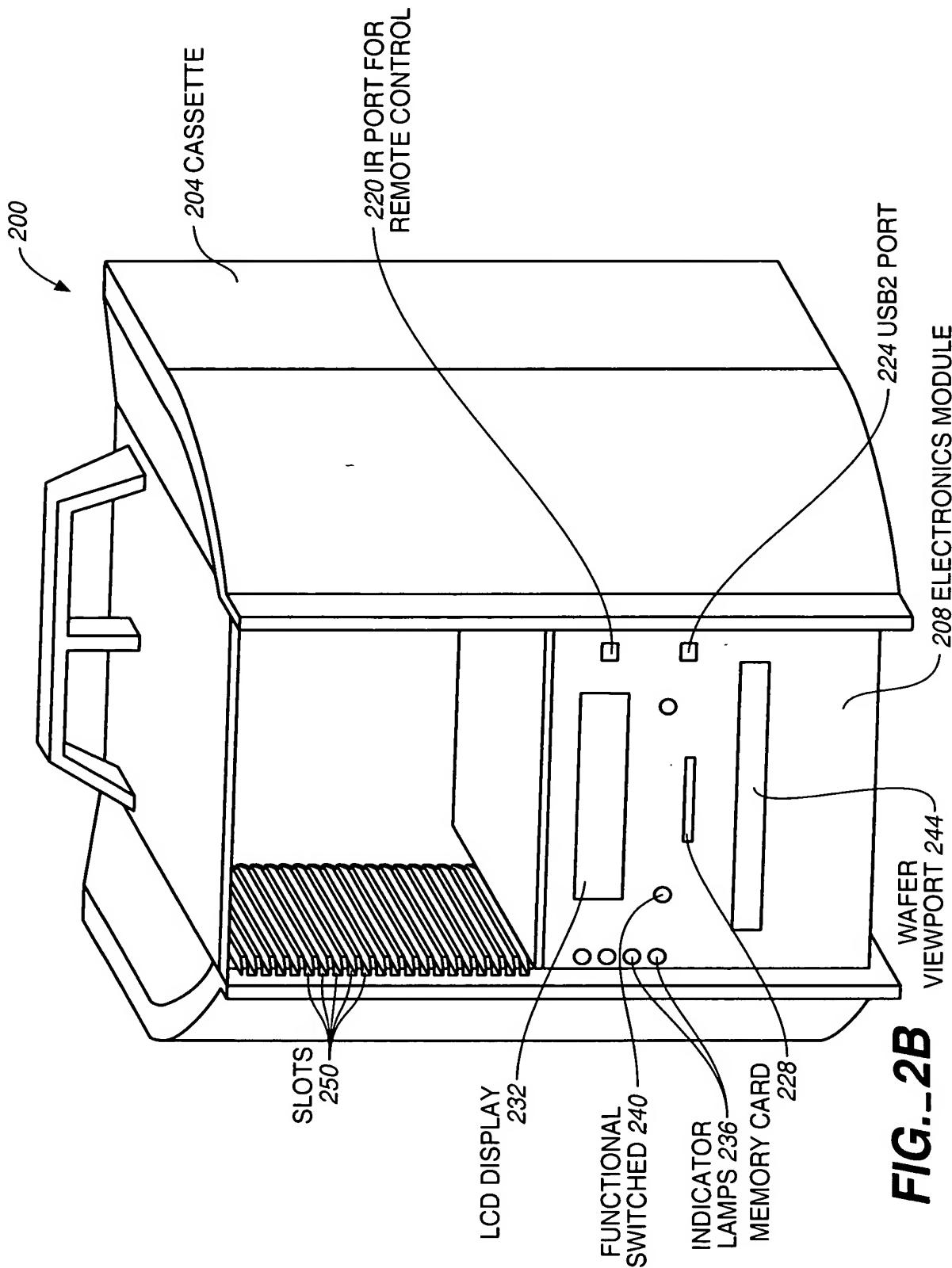
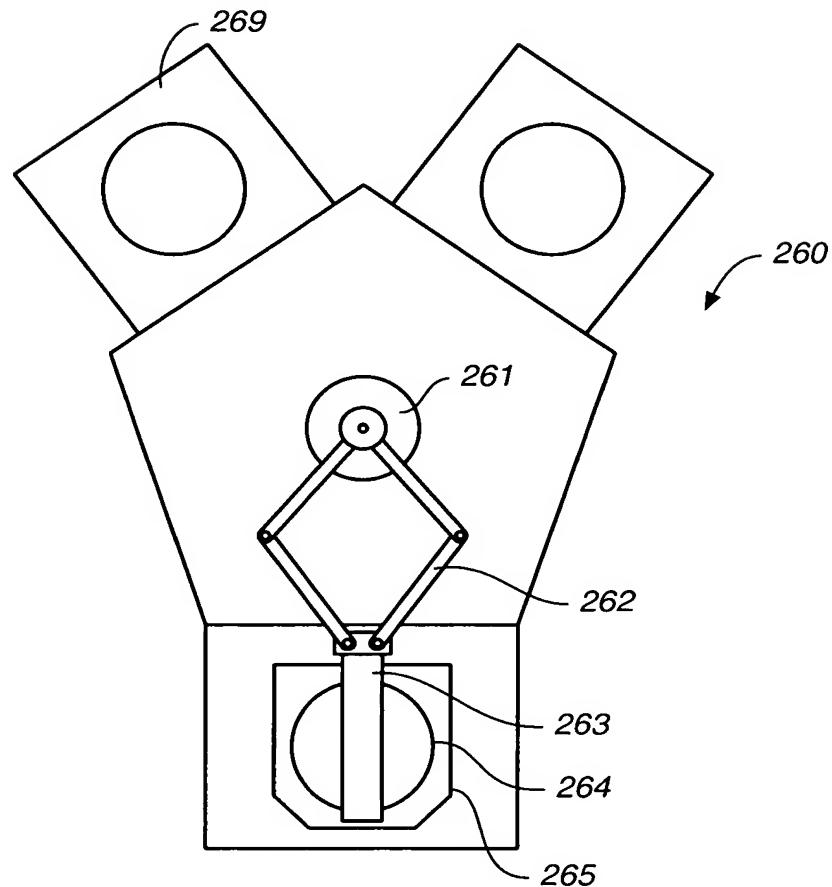


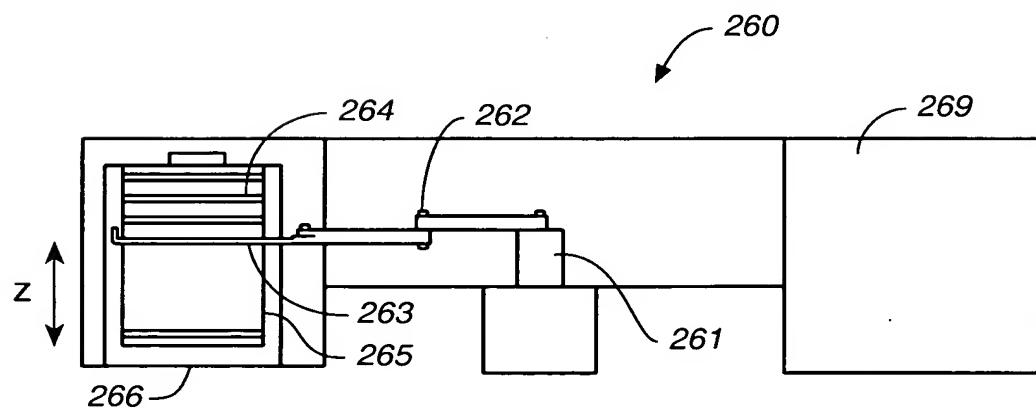
FIG.-2A



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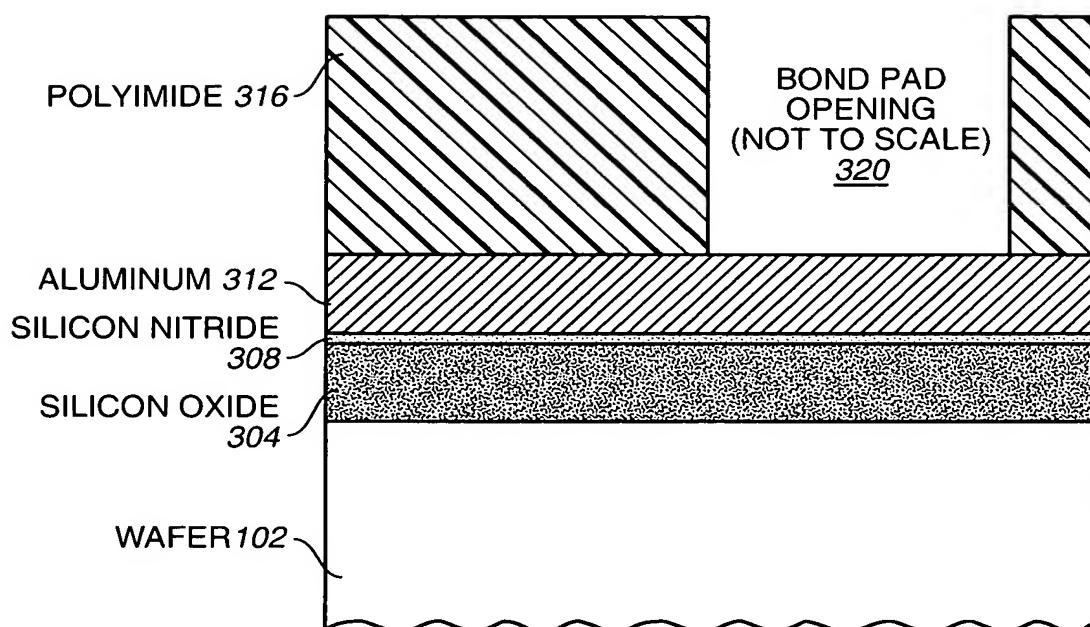


**FIG.\_2C**

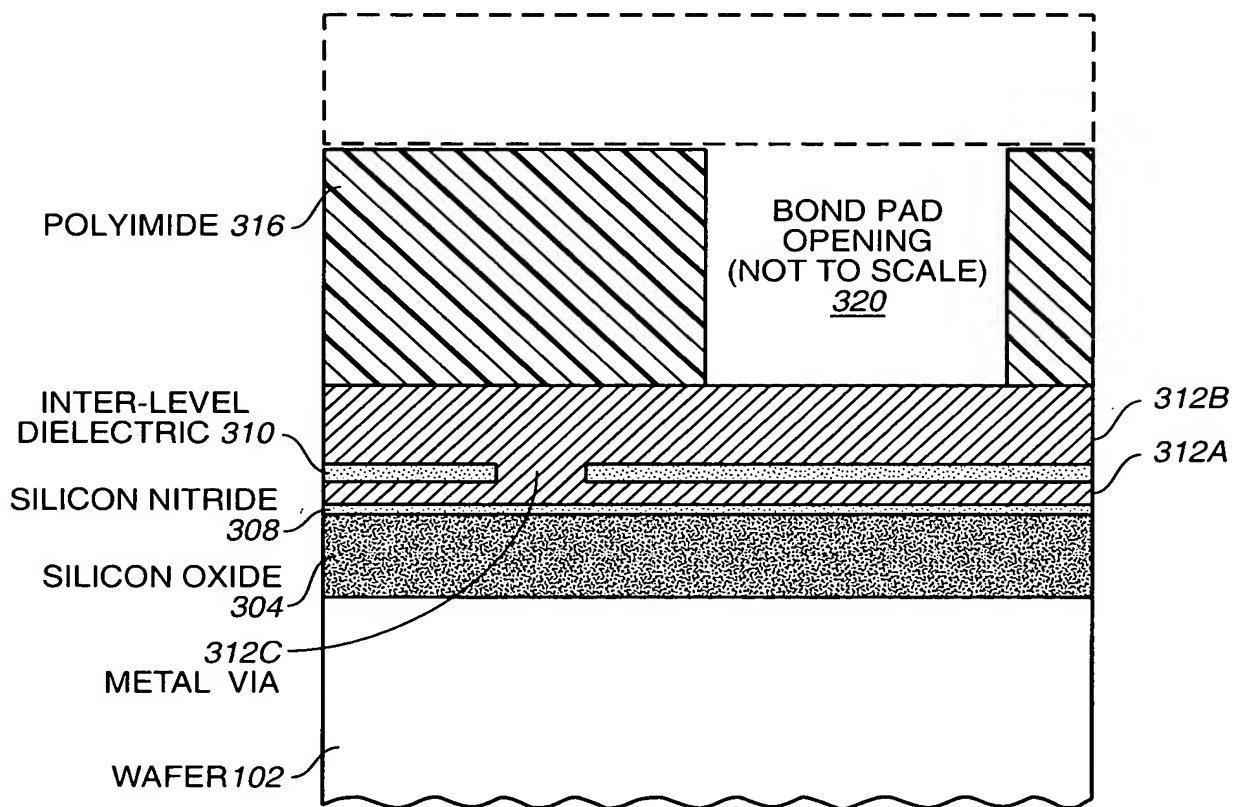


**FIG.\_2D**

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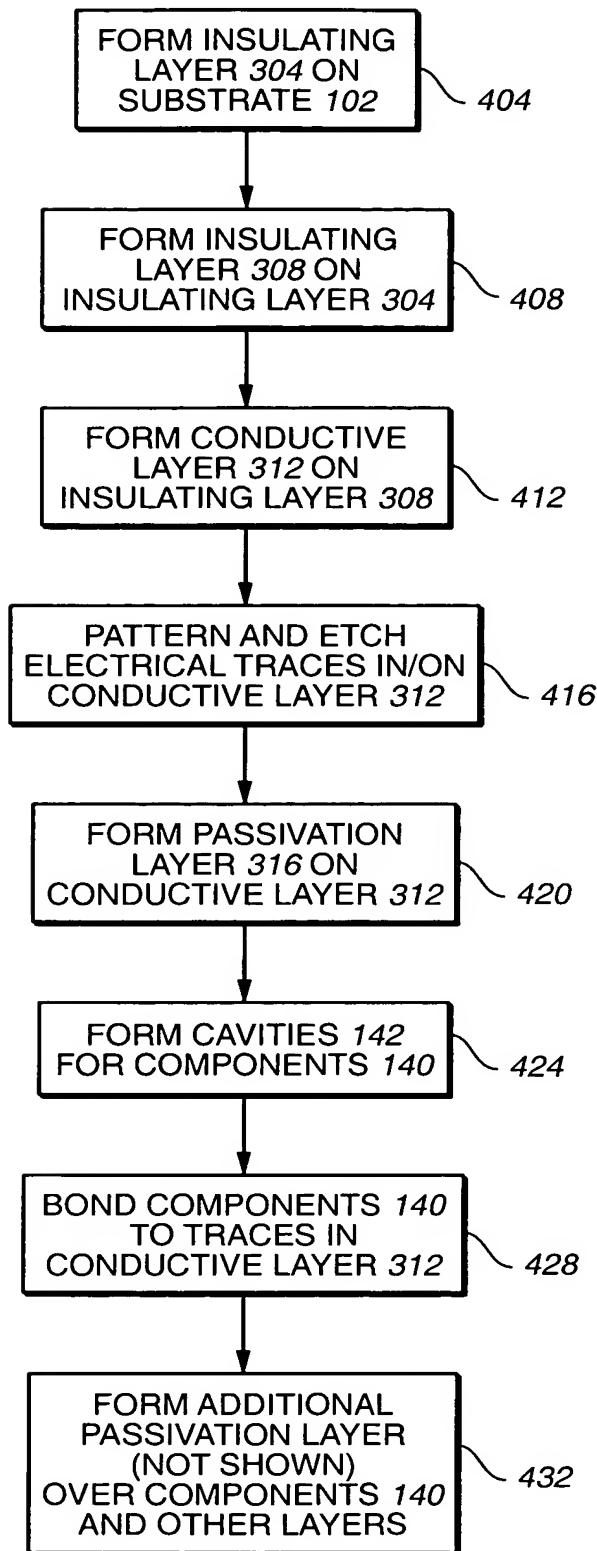


**FIG. 3A**



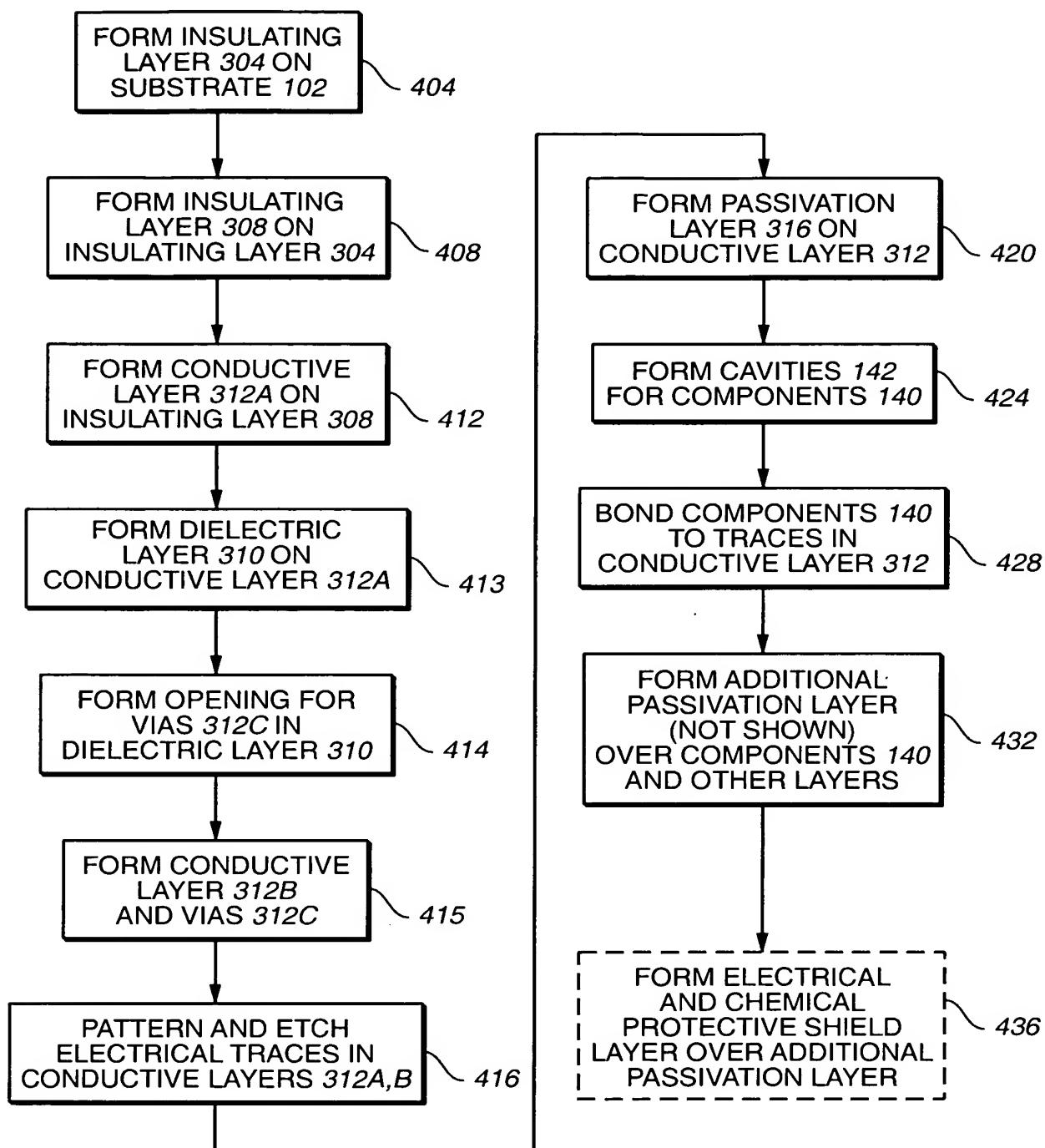
**FIG. 3B**

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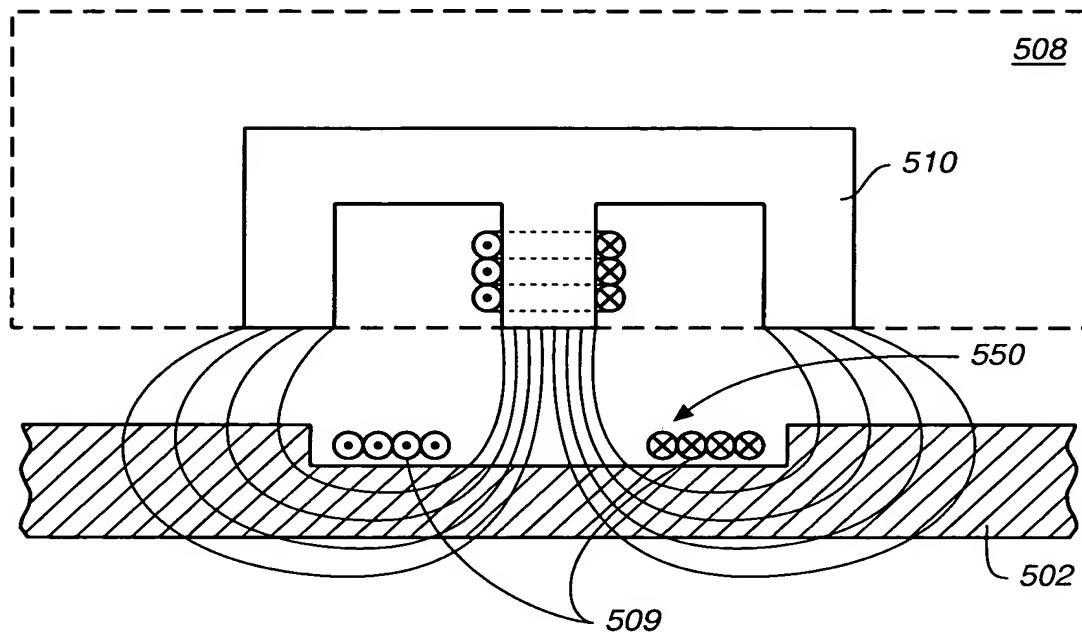
**FIG.\_4A**

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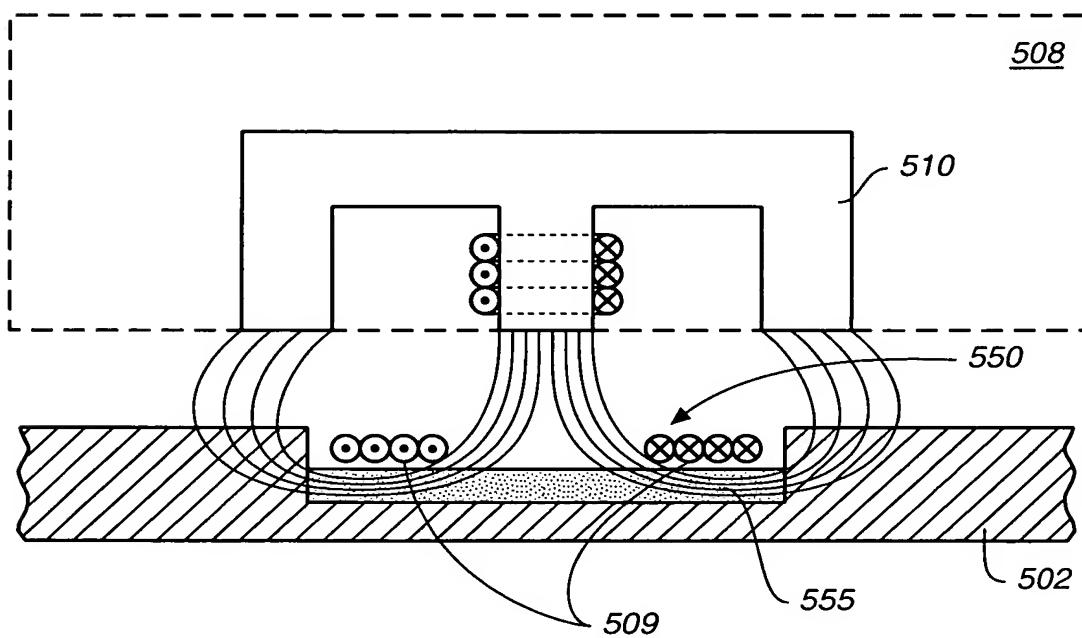


**FIG.\_4B**

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**FIG.\_5A**



**FIG.\_5B**

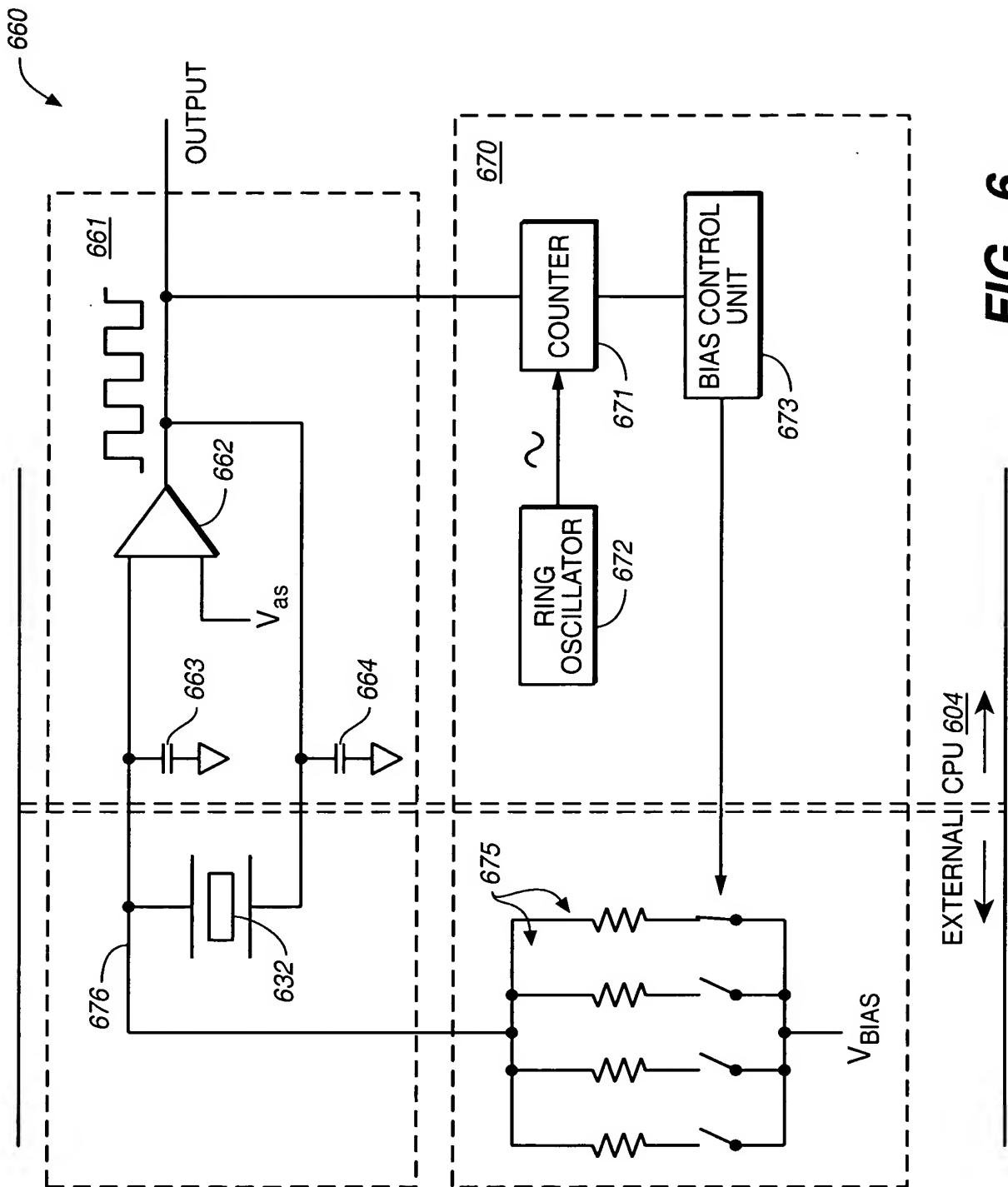
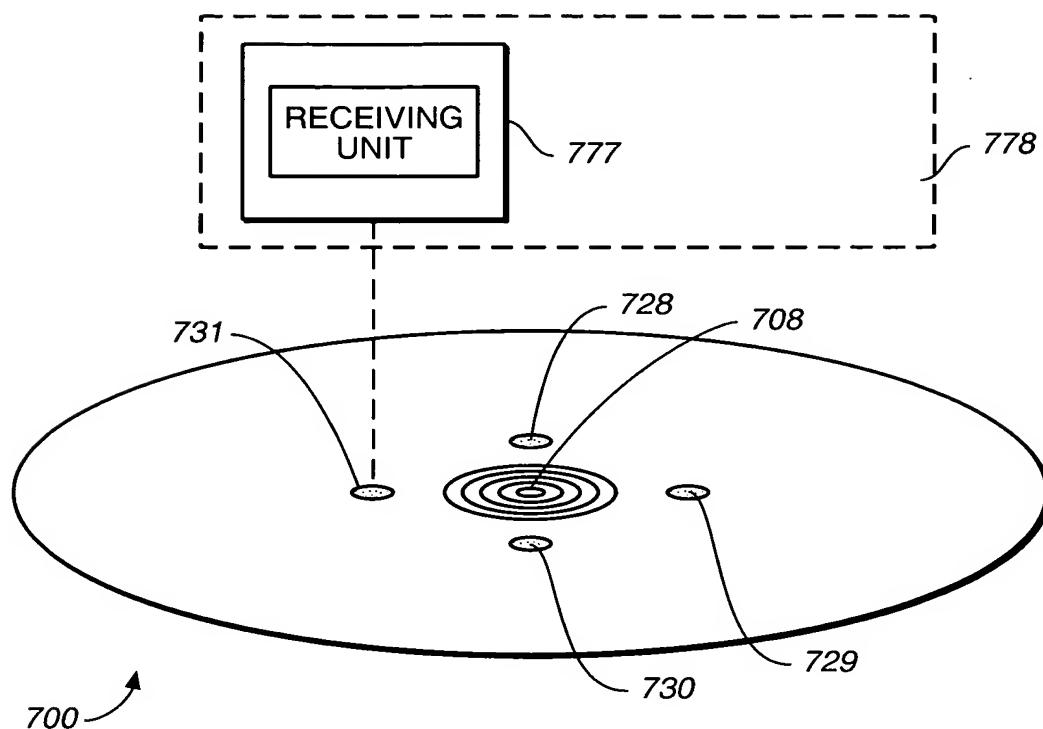
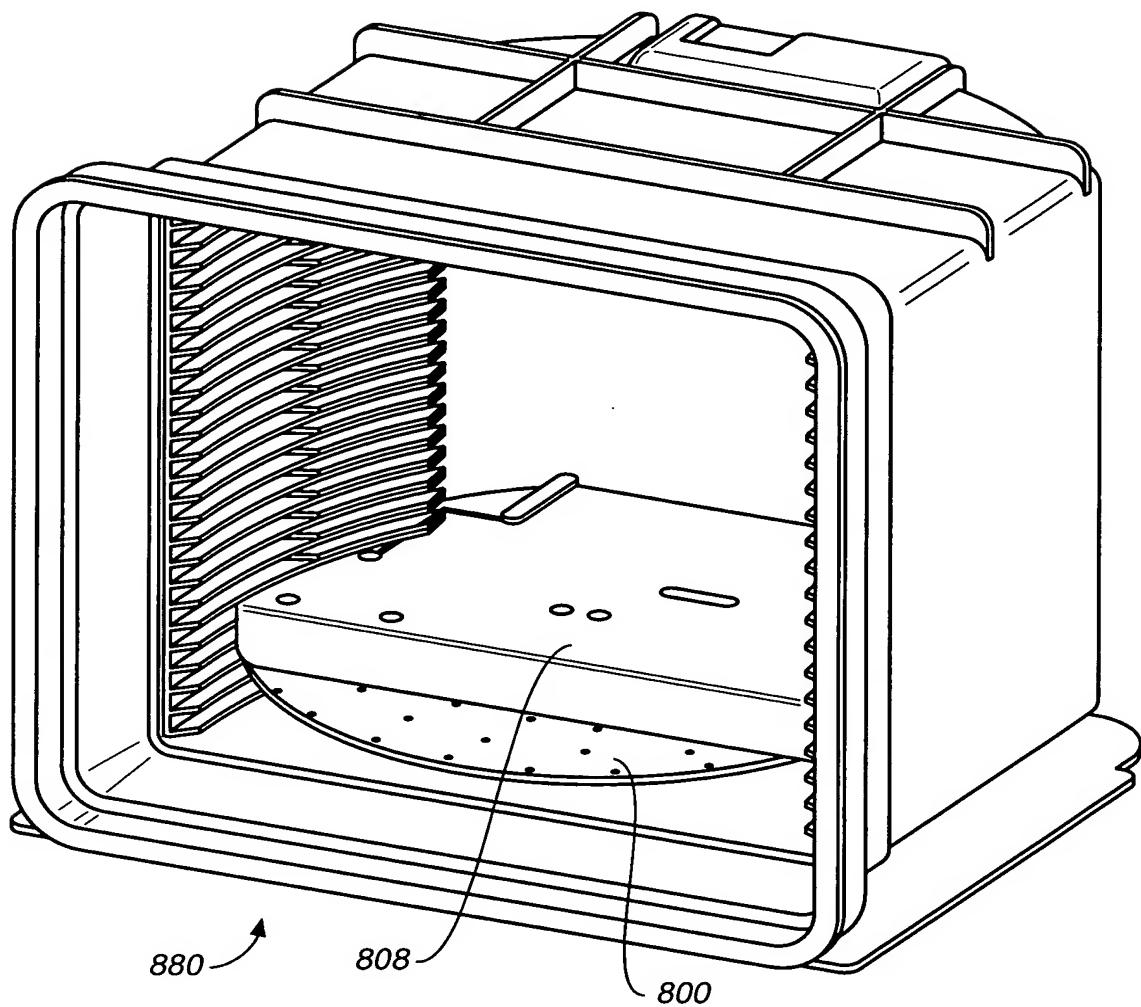


FIG. 6

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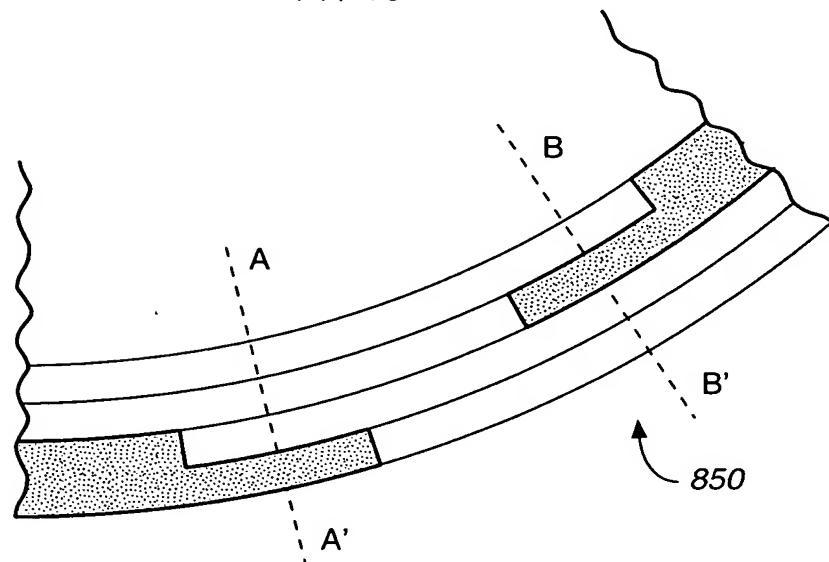


**FIG. 7**

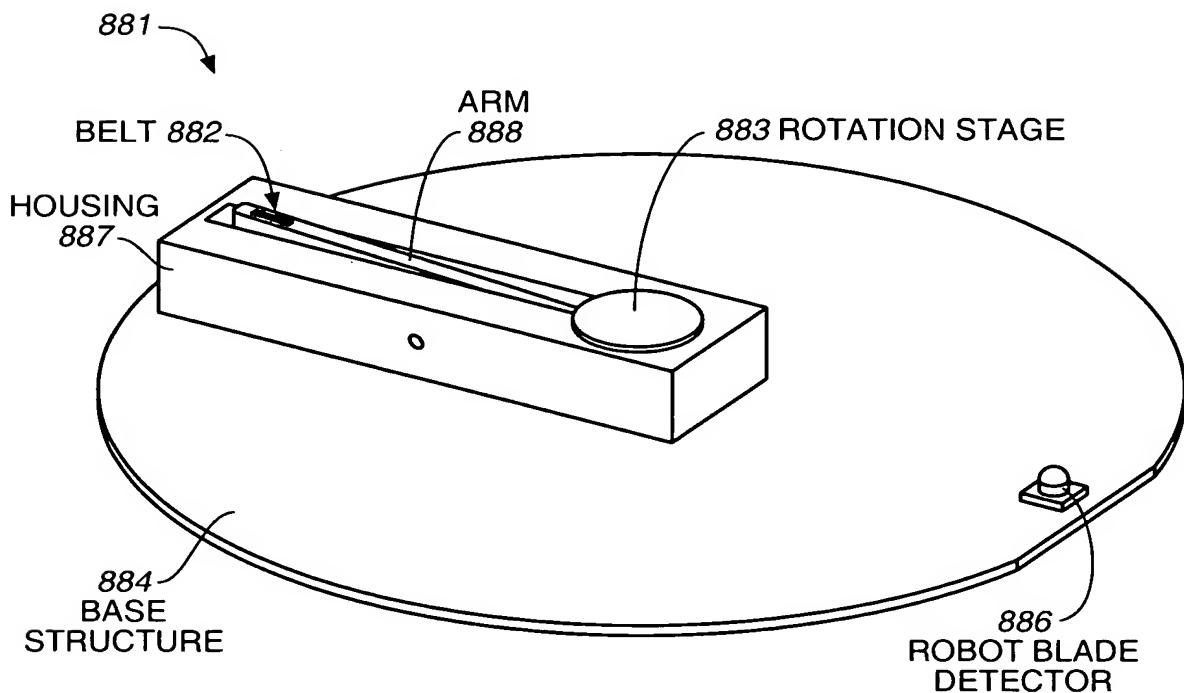


**FIG.\_8A**

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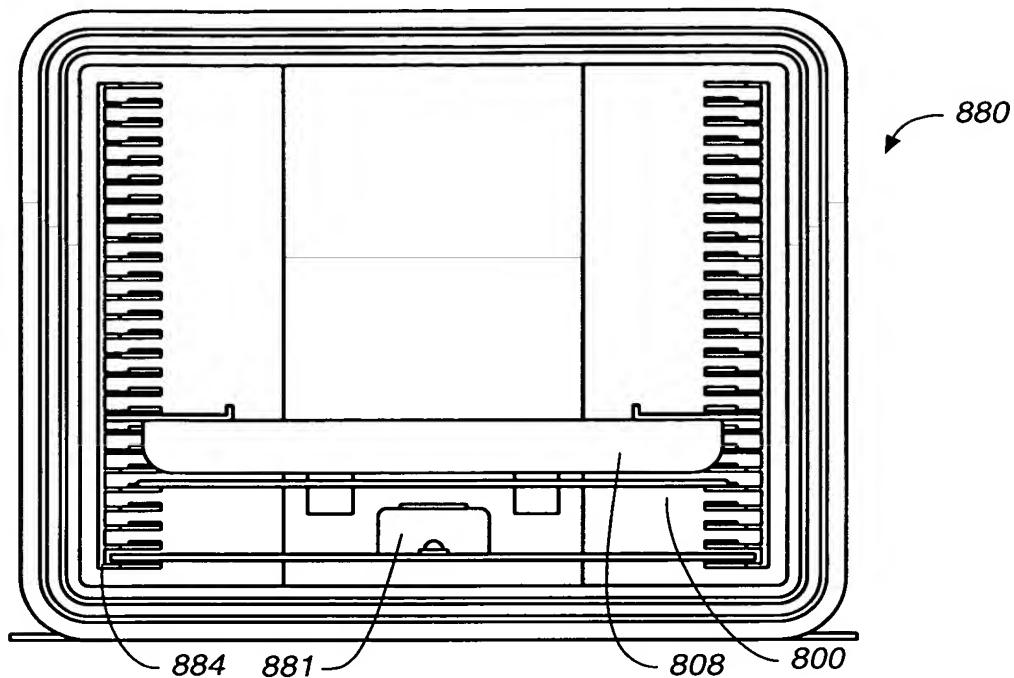


**FIG.\_8B**



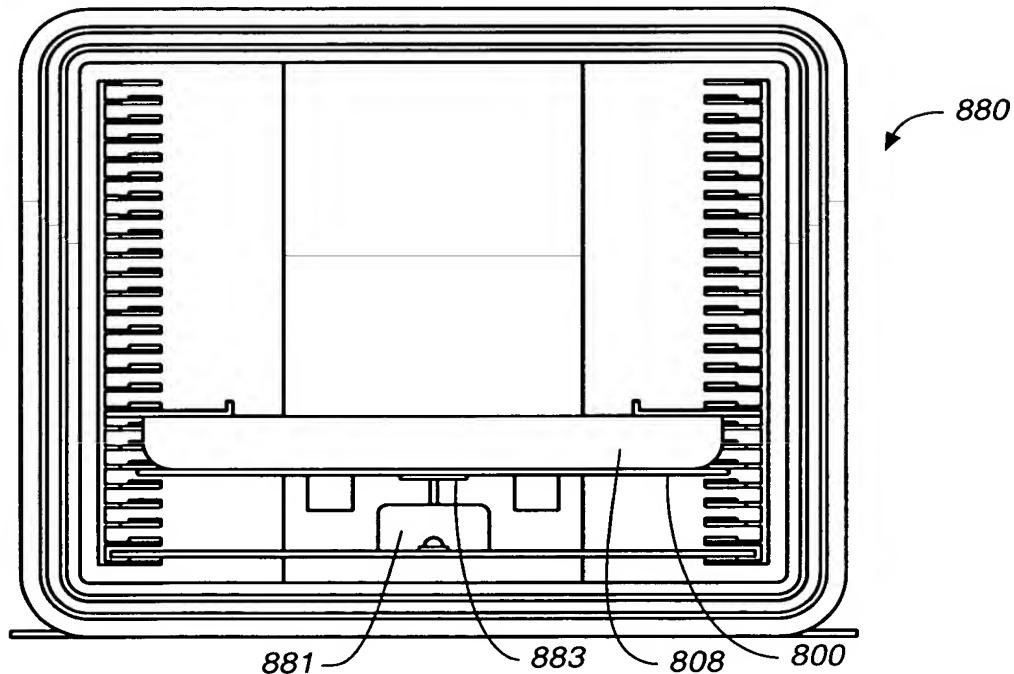
**FIG.\_8C**

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WAFER IN SLOT READY TO PICK UP

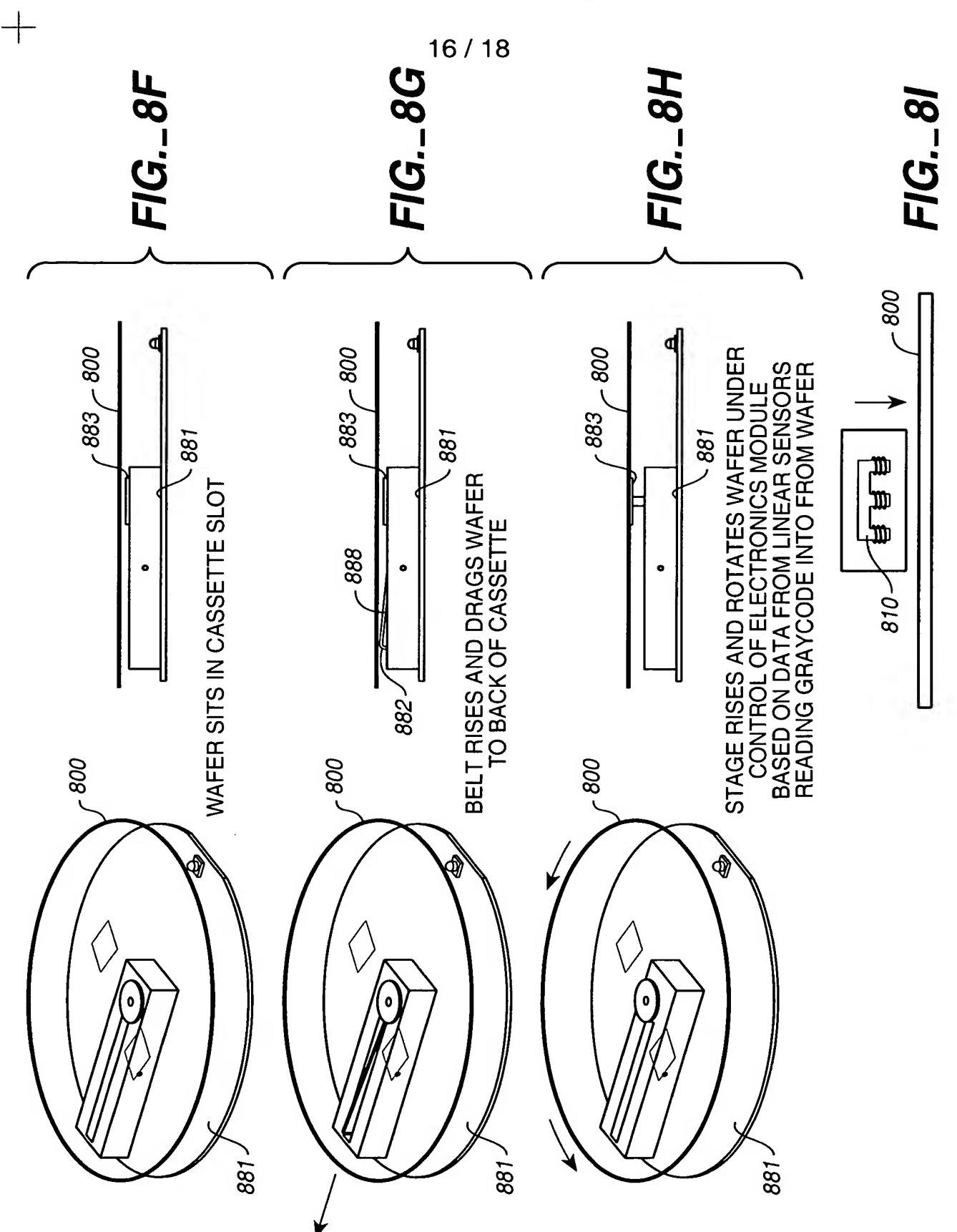
**FIG.\_8D**



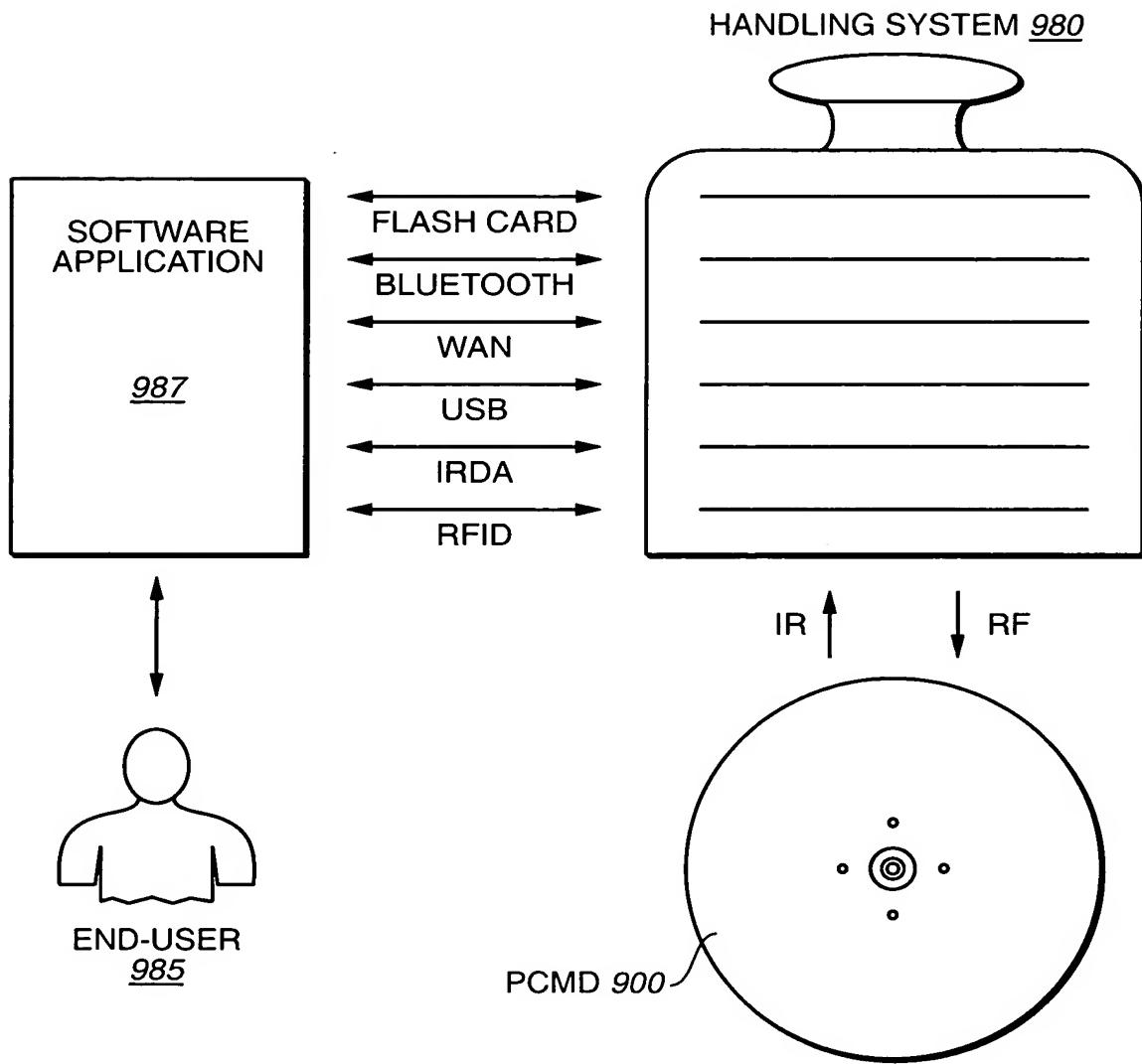
WAFER RAISED FOR ROTATION ALIGNMENT AND CHARGING

**FIG.\_8E**

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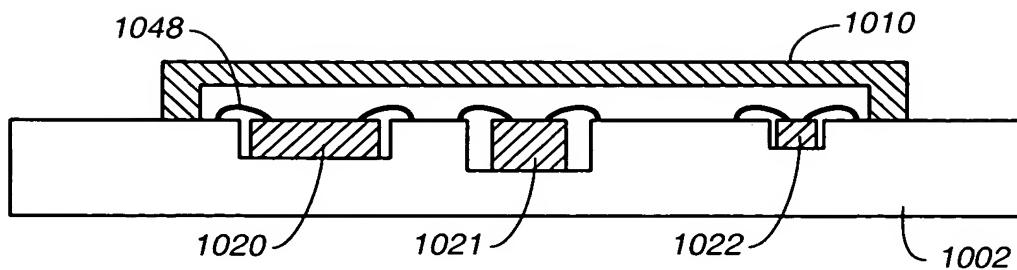


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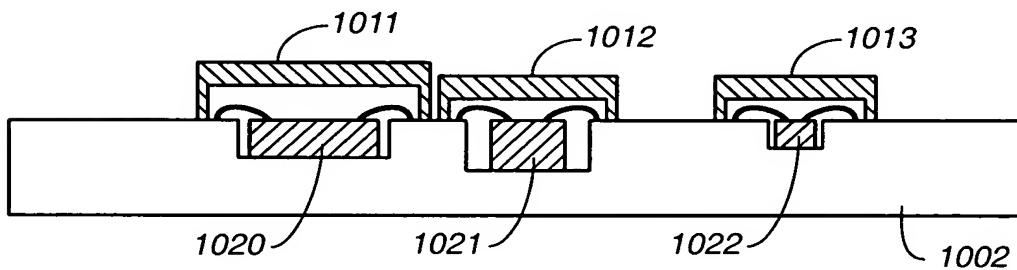


**FIG.\_9**

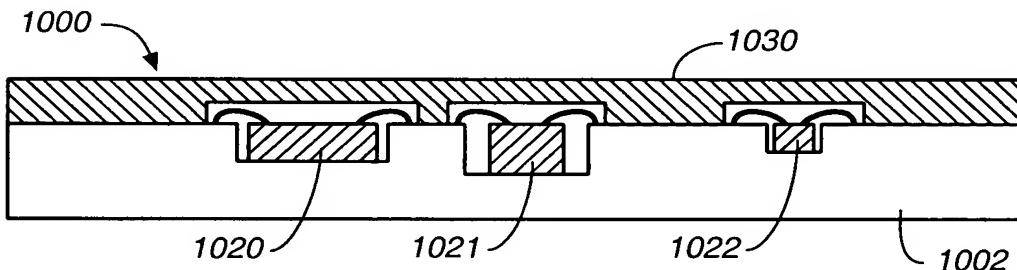
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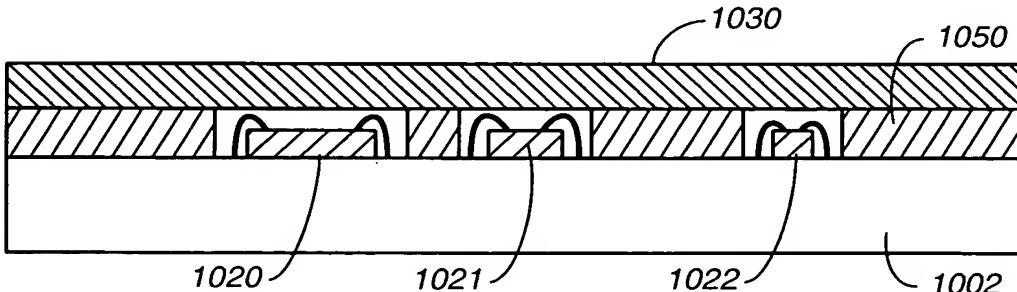
**FIG. 10A**



**FIG. 10B**



**FIG. 10C**



**FIG. 10D**